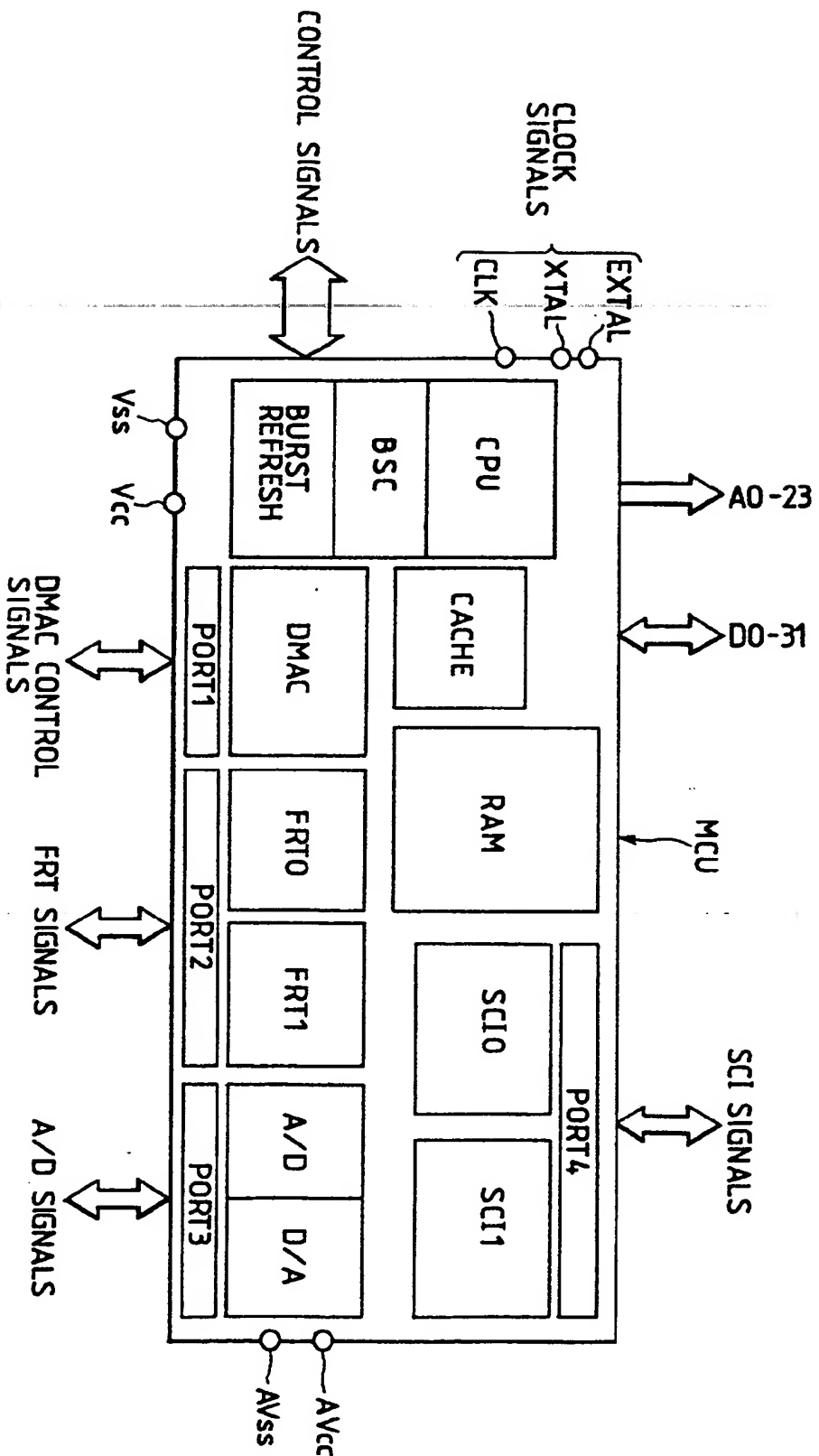


	L #	Hits	Search Text	DBs
1	L3	200	shuffle.ab,ti.	USPAT; US-PGPUB
2	L4	35	((data instruction) near10 shuffle).ab,ti.	USPAT; US-PGPUB
3	L5	12	3 and mask	USPAT; US-PGPUB

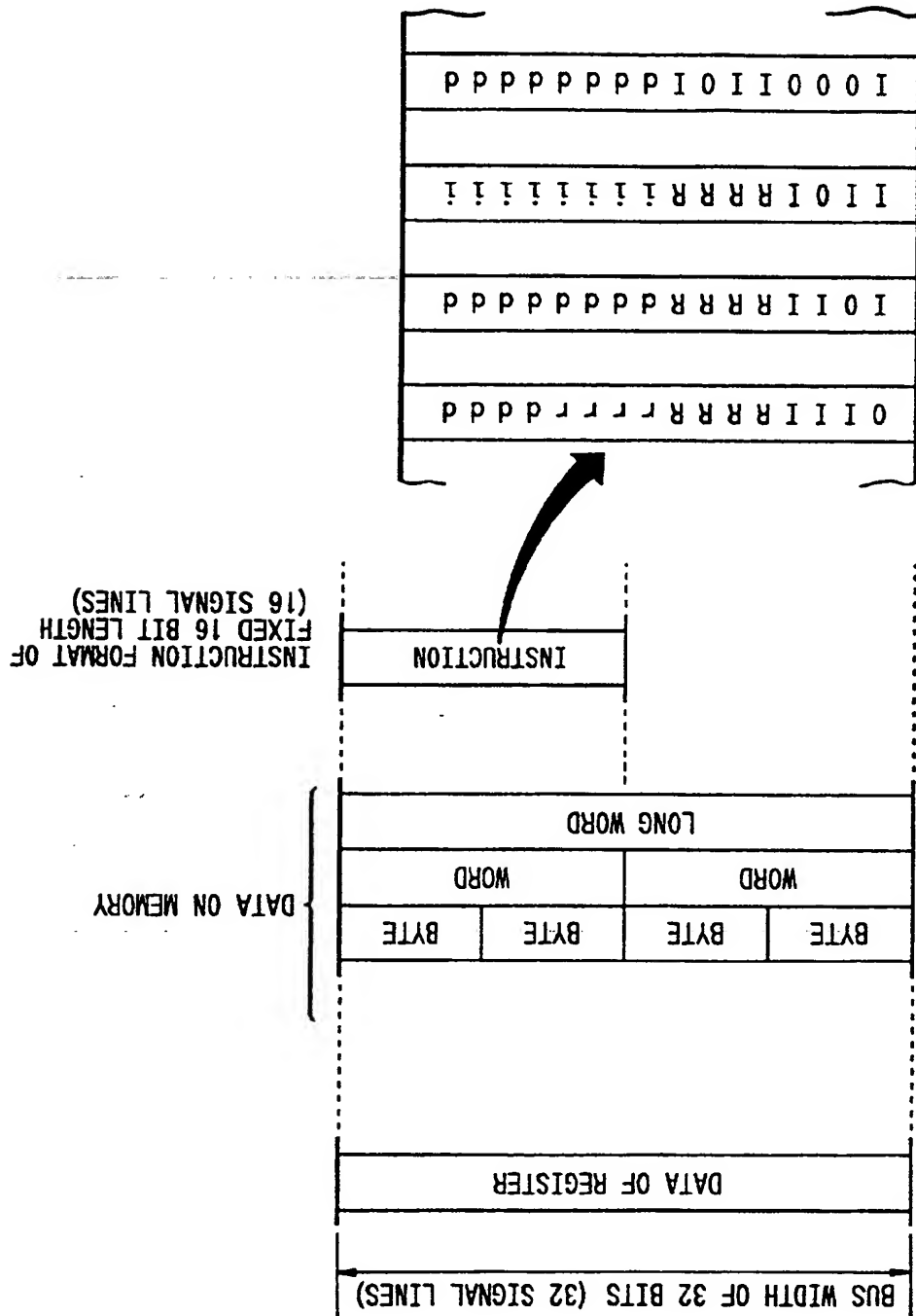
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FIG. 3



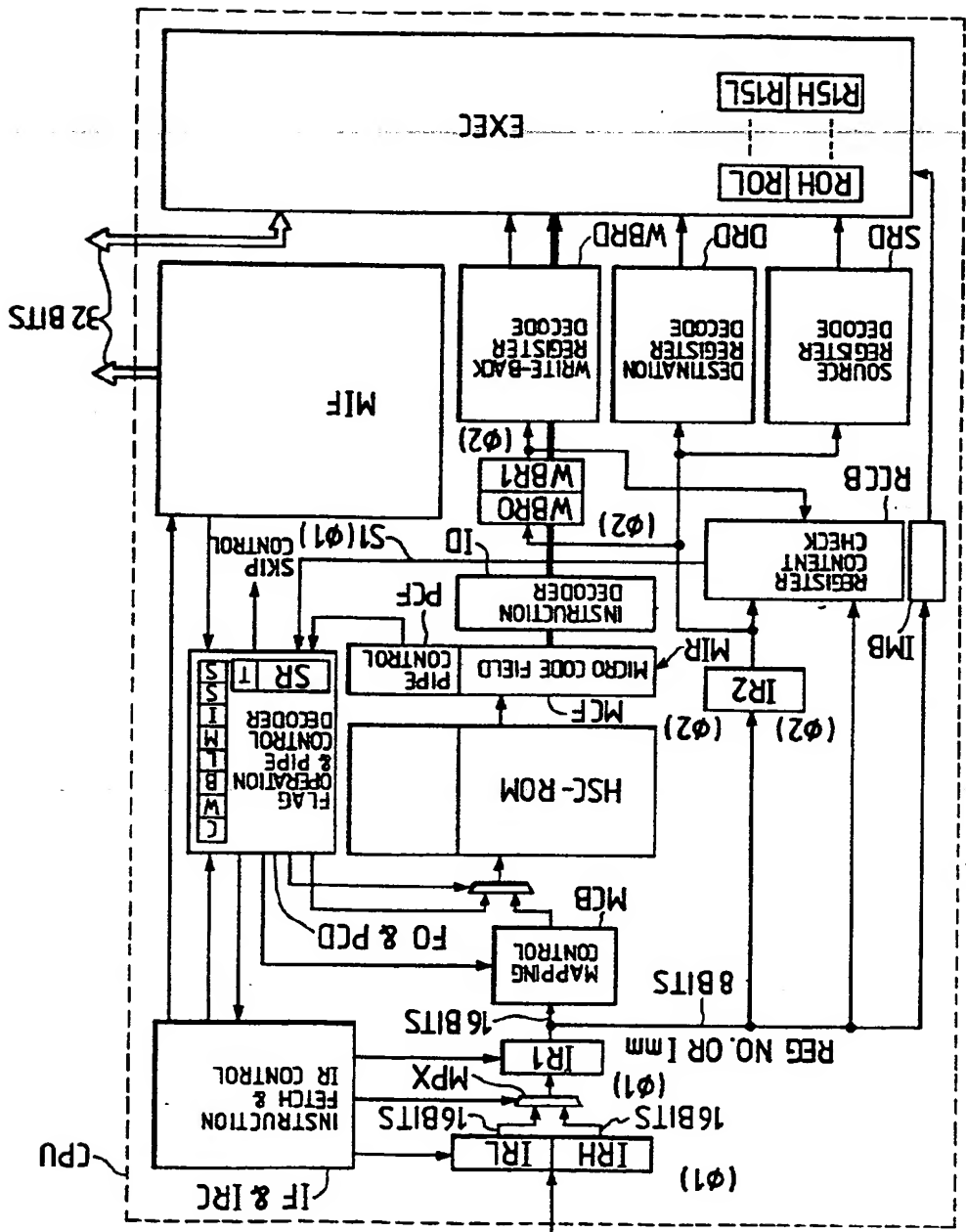
	Docum ent ID	U	Title	Current OR
1	US 20040 05487 9 A1	<input type="checkbox"/>	Method and apparatus for parallel table lookup using SIMD instructions	712/221
2	US 20030 21272 7 A1	<input type="checkbox"/>	Apparatus and method for SIMD modular multiplication	708/620
3	US 20030 13743 9 A1	<input type="checkbox"/>	Shuffler apparatus and related dynamic element matching technique for linearization of unit-element digital-to-analog converters	341/143
4	US 20030 09554 7 A1	<input type="checkbox"/>	2n-1 Shuffling network	370/386
5	US 20030 08408 2 A1	<input type="checkbox"/>	Apparatus and method for efficient filtering and convolution of content data	708/420
6	US 20030 06540 6 A1	<input type="checkbox"/>	Control method to make IC's operation mode to be provided with uncertainty	700/11
7	US 20030 04655 9 A1	<input type="checkbox"/>	Apparatus and method for a data storage device with a plurality of randomly located data	713/189
8	US 20020 19423 5 A1	<input type="checkbox"/>	Processing apparatus	708/403
9	US 20020 11214 7 A1	<input type="checkbox"/>	Shuffle instructions	712/223
10	US 20020 00745 7 A1	<input type="checkbox"/>	Verifiable, secret shuffles of encrypted data, such as elgamal encrypted data for secure multi-authority elections	713/180
11	US 67316 36 B1	<input type="checkbox"/>	Scheduler using small sized shuffle pattern in ATM network	370/395 .1
12	US 64773 13 B1	<input type="checkbox"/>	Disc control method and disc control unit	386/52
13	US 63775 66 B1	<input type="checkbox"/>	OFDM subcarrier hopping in a multi service OFDM system	370/343
14	US 62601 70 B1	<input type="checkbox"/>	Method for controlling memory and digital recording/reproducing device using the same	714/769
15	US 61782 89 B1	<input type="checkbox"/>	Video data shuffling method and apparatus	386/112
16	US 61733 30 B1	<input type="checkbox"/>	Delivery and acquisition of data segments with optimized inter-arrival time	709/232
17	US 61449 86 A	<input type="checkbox"/>	System for sorting in a multiprocessor environment	709/201
18	US 61084 87 A	<input type="checkbox"/>	Shuffle encoded video data recording and reproduction	386/124
19	US 60414 04 A	<input type="checkbox"/>	Dual function system and method for shuffling packed data elements	712/210

FIG. 4

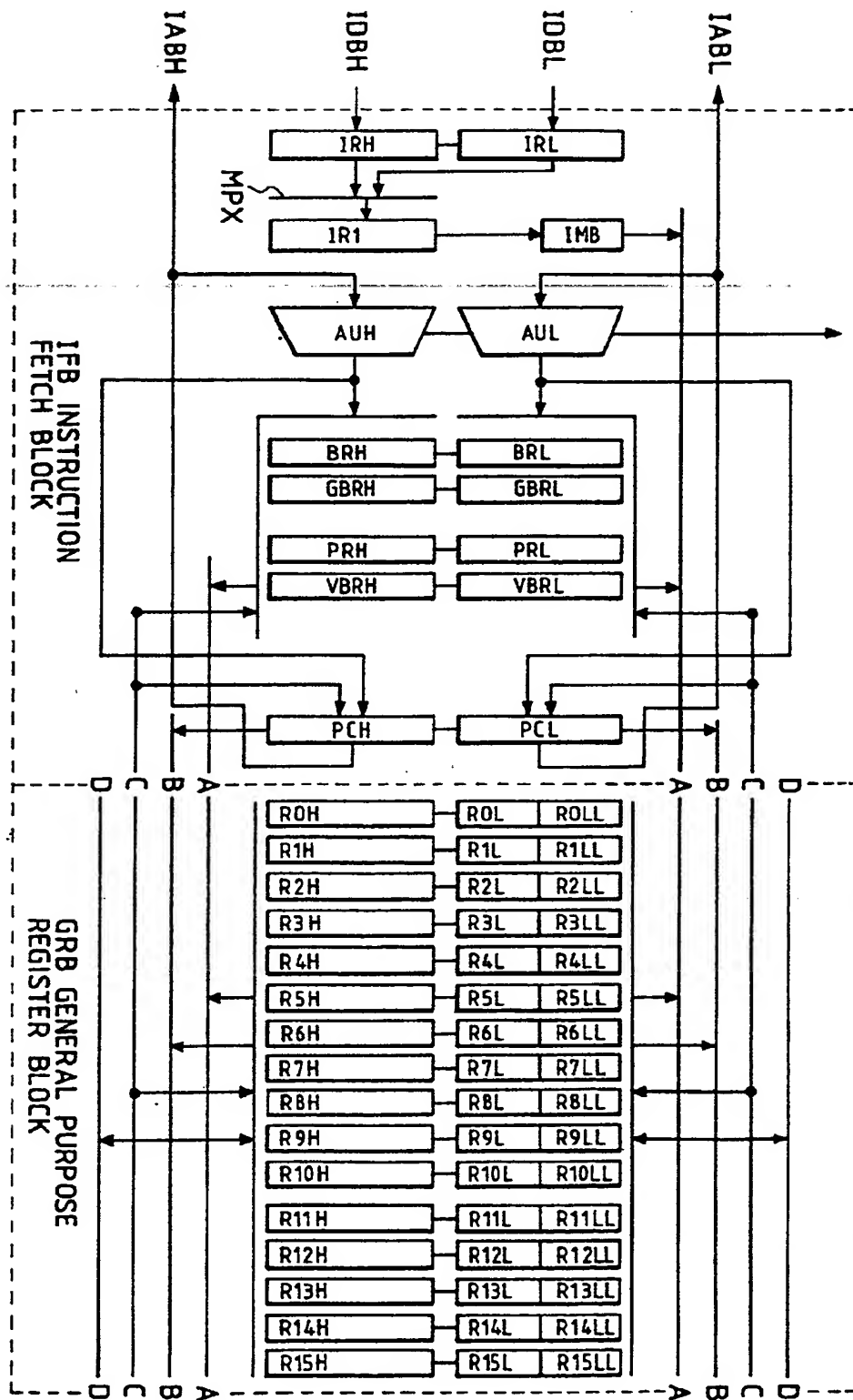


	Docum ent ID	U	Title	Current OR
20	US 60379 89 A	<input type="checkbox"/>	Still image transmitting device	348/441
21	US 59303 65 A	<input type="checkbox"/>	Digital data shuffling/de-shuffling system	380/28
22	US 59056 60 A	<input type="checkbox"/>	Discrete cosine transform circuit for processing an 8.times.8 block and two 4.times.8 blocks	708/402
23	US 58420 34 A	<input type="checkbox"/>	Two dimensional crossbar mesh for multi-processor interconnect	712/11
24	US 58417 81 A	<input type="checkbox"/>	Data shuffling method and apparatus for same	714/701
25	US 57346 49 A	<input type="checkbox"/>	Data packet router	370/355
26	US 56129 64 A	<input type="checkbox"/>	High performance, fault tolerant orthogonal shuffle memory and method	714/763
27	US 55352 75 A	<input type="checkbox"/>	Apparatus and method for producing scrambled digital video signals	380/203
28	US 54524 66 A	<input type="checkbox"/>	Method and apparatus for preforming DCT and IDCT transforms on data signals with a preprocessor, a post-processor, and a controllable shuffle-exchange unit connected between the pre-processor and post-processor	712/36
29	US 54407 06 A	<input type="checkbox"/>	Data shuffling apparatus possessing reduced memory	711/202
30	US 51970 62 A	<input type="checkbox"/>	Method and system for simultaneous analysis of multiplexed channels	370/508
31	US 49724 95 A	<input type="checkbox"/>	Feature extraction processor	382/304
32	US 46010 06 A	<input type="checkbox"/>	Architecture for two dimensional fast fourier transform	708/401
33	US 43853 63 A	<input type="checkbox"/>	Discrete cosine transformer	708/402
34	US 41610 36 A	<input type="checkbox"/>	Method and apparatus for random and sequential accessing in dynamic memories	711/159
35	US 38101 12 A	<input checked="" type="checkbox"/>	SHIFT-SHUFFLE MEMORY SYSTEM WITH RAPID SEQUENTIAL ACCESS	365/230 .09

FIG. 5



	Docum ent ID	U	Title	Current OR
1	US 20040 05487 9 A1	<input type="checkbox"/>	Method and apparatus for parallel table lookup using SIMD instructions	712/221
2	US 20030 21272 7 A1	<input type="checkbox"/>	Apparatus and method for SIMD modular multiplication	708/620
3	US 20030 12822 2 A1	<input type="checkbox"/>	Multisample dithering with shuffle tables	345/611
4	US 20030 08408 2 A1	<input type="checkbox"/>	Apparatus and method for efficient filtering and convolution of content data	708/420
5	US 20030 04655 9 A1	<input type="checkbox"/>	Apparatus and method for a data storage device with a plurality of randomly located data	713/189
6	US 20030 03781 2 A1	<input type="checkbox"/>	Decontamination and contaminant processing system and method	135/96
7	US 20020 11214 7 A1	<input type="checkbox"/>	Shuffle instructions	712/223
8	US 64113 02 B1	<input type="checkbox"/>	Method and apparatus for addressing multiple frame buffers	345/545
9	US 61449 86 A	<input type="checkbox"/>	System for sorting in a multiprocessor environment	709/201
10	US 60414 04 A	<input type="checkbox"/>	Dual function system and method for shuffling packed data elements	712/210
11	US 58926 20 A	<input type="checkbox"/>	Optical shuffle device	359/566
12	US 49724 95 A	<input type="checkbox"/>	Feature extraction processor	382/304



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